



- 1 -

Docket No. 0756-2428

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of) Art Unit: 2811
Osamu NAKAMURA ET AL.) Examiner: Unassigned
Serial No. 10/056,055)
Filed: January 28, 2002)
For: SEMICONDUCTOR DEVICE AND)
MANUFACTURING METHOD OF)
THE SAME)

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INFORMATION DISCLOSURE STATEMENT

AND NOTIFICATION OF RELATED APPLICATION

Commissioner for Patents
Washington, DC 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be considered by the Examiner and made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The documents are being submitted before the first Office Action on the merits, therefore no fee or certification is required under 37 C.F.R. § 1.97(b).

The Examiner's attention is direct to related application serial numbers 10/097,641, 10/072,931, 10/074,050, 10/051,064, 10/066,542, and 10/020,961. which are listed on the Form PTO-1449 submitted herewith.

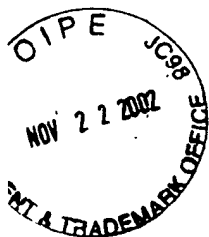
The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 50-2280.

Respectfully submitted,



Eric J. Robinson
Reg. No. 38,285

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11/12/02
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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/056,055
	Filing Date	January 28, 2002
	First Named Inventor	Osamu NAKAMURA et al.
	Group Art Unit	2811
	Examiner Name	Unassigned
Total Number of Pages in This Submission	Attorney Docket Number	0756-2428

ENCLOSURES (check all that apply)		
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Remarks		<input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees required or credit any overpayments to Deposit Account No. 50-2280 for the above identified docket number.

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Eric J. Robinson, Reg. No. 38,285 Robinson Intellectual Property Law Office, P.C. PMB 955 21010 Southbank Street Potomac Falls, VA 20165
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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/056,055
				Filing Date	January 28, 2002
				First Named Inventor	Osamu NAKAMURA et al.
				Group Art Unit	2811
				Examiner Name	Unassigned
Sheet	1	of	3	Attorney Docket Number	0756-2428

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
		6,426,276	B1	Ohnuma et al.	07/30/2002	
		6,396,147	B1	Adachi et al.	05/28/2002	
		6,461,943	B1	Yamazaki et al.	10/08/2002	
		6,376,336	B1	Buynoski	04/23/2002	
		6,255,195	B1	Linn et al.	07/03/2001	
		4,371,403		Ikubo et al.	02/01/1983	
		2001/0034088	A1	Nakamura et al.	10/25/2001	
		5,932,893		Miyanaga et al.	08/03/1999	
		6,022,458		Ichikawa	02/08/2000	
		5,705,829		Miyanaga et al.	01/06/1998	
		3,535,775		Garfinkel et al.	10/27/1970	
		4,477,308		Gibson et al.	10/16/1984	
		4,534,820		Mori et al.	08/13/1985	
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		5,426,064		Zhang et al.	06/20/1995	
		5,481,121		Zhang et al.	01/02/1996	
		5,488,000		Zhang et al.	01/30/1996	
		5,492,843		Adachi et al.	02/20/1996	
		5,501,989		Takayama et al.	03/26/1996	
		5,508,533		Takemura	04/16/1996	
		5,534,716		Takemura	07/09/1996	
		5,543,352		Ohtani et al.	08/06/1996	
		5,563,426		Zhang et al.	10/08/1996	
		5,569,610		Zhang et al.	10/29/1996	
		5,569,936		Zhang et al.	10/29/1996	
		5,580,792		Zhang et al.	12/03/1996	
		5,585,291		Ohtani et al.	12/17/1996	
		5,589,694		Takayama et al.	12/31/1996	

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				Group Art Unit	2811
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Sheet	2	of	3	Attorney Docket Number	0756-2428

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		Number	Kind Code ² (if known)			
		5,595,923		Zhang et al.	01/21/1997	
		5,595,944		Zhang et al.	01/21/1997	
		5,604,360		Zhang et al.	02/18/1997	
		5,605,846		Ohtani et al.	02/25/1997	
		5,606,179		Yamazaki et al.	02/25/1997	
		5,612,250		Ohtani et al.	03/18/1997	
		5,614,426		Funada et al.	03/25/1997	
		5,614,733		Zhang et al.	03/25/1997	
		5,616,506		Takemura	04/01/1997	
		5,620,910		Teramoto	04/15/1997	
		5,621,224		Yamazaki et al.	04/15/1997	
		5,624,851		Takayama et al.	04/29/1997	
		5,637,515		Takemura	06/10/1997	
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		5,654,203		Ohtani et al.	08/05/1997	
		5,656,825		Kusumoto et al.	08/12/1997	
		5,663,077		Adachi et al.	09/02/1997	
		5,696,386		Yamazaki	12/09/1997	
		5,696,388		Funada et al.	12/09/1997	
		5,712,191		Nakajima et al.	01/27/1998	
		5,744,824		Kousai et al.	04/28/1998	
		5,843,225		Takayama et al.	12/01/1998	
		5,677,549		Takayama et al.	10/14/1997	

FOREIGN PATENT DOCUMENTS							
Examiner Initials ¹	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Office ³	Number ⁴	Kind Code ⁵ (if known)			
		JP	07-130652			05/19/1995	Abstract
		JP	08-078329			03/22/1996	Abstract

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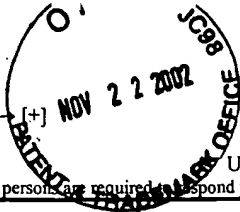
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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
		JP	09-074207			03/18/1997		Full
		JP	2000-260777			09/22/2000		Abstract
		JP	2001-210828			08/03/2001		Abstract

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		U.S. Patent Application Serial No. 10/097,641, including specification, drawings and filing receipt, "Method of Manufacturing a Semiconductor Device", Shunpei YAMAZAKI et al., filed March 15, 2002.	
		U.S. Patent Application Serial No. 10/072,931, including specification, drawings and filing receipt, "Method of Manufacturing a Semiconductor Device", Shunpei YAMAZAKI et al., filed February 12, 2002.	
		U.S. Patent Application Serial No. 10/074,050, including specification, drawings and filing receipt, "Method of Manufacturing a Semiconductor Device", Shunpei YAMAZAKI et al., filed February 14, 2002.	
		U.S. Patent Application Serial No. 10/051,064, including specification, drawings and filing receipt, "Semiconductor Device and Method of Manufacturing the Same", filed January 18, 2002.	
		U.S. Patent Application Serial No. 10/066,542, including specification, drawings and filing receipt, "Semiconductor Device and Method for Manufacturing the Same", filed February 5, 2002.	
		U.S. Patent Application Serial No. 10/020,961, including specification, drawings and filing receipt, "Method of Manufacturing Semiconductor Device and Semiconductor Device", filed December 19, 2001.	
		M. Miyake et al., "Characteristics of Buried-Channel pMOS Devices with Shallow Counter-Doped Layers Fabricated Using Channel Preamorphization", IEEE Transactions on Electron Devices, Vol. 43, No. 3, March 1996, pp. 444-449.	
		L. S. Lee et al., "Argon Ion-Implantation on Polysilicon or Amorphous-Silicon for Boron Penetration Suppression in p+ pMOSFET", IEEE Transactions on Electron Devices, Vol. 45, No. 8, August 1998, pp. 1737-1744.	

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